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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/051,567	01/18/2002	Yoshiharu Hashimoto	15227	3382
23389	7590	12/05/2006	EXAMINER	
SCULLY SCOTT MURPHY & PRESSER, PC 400 GARDEN CITY PLAZA SUITE 300 GARDEN CITY, NY 11530				KUMAR, SRILAKSHMI K
ART UNIT		PAPER NUMBER		
		2629		

DATE MAILED: 12/05/2006

Please find below and/or attached an Office communication concerning this application or proceeding.

**Advisory Action
Before the Filing of an Appeal Brief**

Application No.	10/051,567	Applicant(s) HASHIMOTO, YOSHIHARU
Examiner Srilakshmi K. Kumar	Art Unit 2629	

--The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

THE REPLY FILED 07 November 2006 FAILS TO PLACE THIS APPLICATION IN CONDITION FOR ALLOWANCE.

1. The reply was filed after a final rejection, but prior to or on the same day as filing a Notice of Appeal. To avoid abandonment of this application, applicant must timely file one of the following replies: (1) an amendment, affidavit, or other evidence, which places the application in condition for allowance; (2) a Notice of Appeal (with appeal fee) in compliance with 37 CFR 41.31; or (3) a Request for Continued Examination (RCE) in compliance with 37 CFR 1.114. The reply must be filed within one of the following time periods:

- a) The period for reply expires 3 months from the mailing date of the final rejection.
 b) The period for reply expires on: (1) the mailing date of this Advisory Action, or (2) the date set forth in the final rejection, whichever is later. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the mailing date of the final rejection.

Examiner Note: If box 1 is checked, check either box (a) or (b). ONLY CHECK BOX (b) WHEN THE FIRST REPLY WAS FILED WITHIN TWO MONTHS OF THE FINAL REJECTION. See MPEP 706.07(f).

Extensions of time may be obtained under 37 CFR 1.136(a). The date on which the petition under 37 CFR 1.136(a) and the appropriate extension fee have been filed is the date for purposes of determining the period of extension and the corresponding amount of the fee. The appropriate extension fee under 37 CFR 1.17(a) is calculated from: (1) the expiration date of the shortened statutory period for reply originally set in the final Office action; or (2) as set forth in (b) above, if checked. Any reply received by the Office later than three months after the mailing date of the final rejection, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

NOTICE OF APPEAL

2. The Notice of Appeal was filed on _____. A brief in compliance with 37 CFR 41.37 must be filed within two months of the date of filing the Notice of Appeal (37 CFR 41.37(a)), or any extension thereof (37 CFR 41.37(e)), to avoid dismissal of the appeal. Since a Notice of Appeal has been filed, any reply must be filed within the time period set forth in 37 CFR 41.37(a).

AMENDMENTS

3. The proposed amendment(s) filed after a final rejection, but prior to the date of filing a brief, will not be entered because
 (a) They raise new issues that would require further consideration and/or search (see NOTE below);
 (b) They raise the issue of new matter (see NOTE below);
 (c) They are not deemed to place the application in better form for appeal by materially reducing or simplifying the issues for appeal; and/or
 (d) They present additional claims without canceling a corresponding number of finally rejected claims.

NOTE: _____. (See 37 CFR 1.116 and 41.33(a)).

4. The amendments are not in compliance with 37 CFR 1.121. See attached Notice of Non-Compliant Amendment (PTOL-324).
 5. Applicant's reply has overcome the following rejection(s): _____.
 6. Newly proposed or amended claim(s) _____ would be allowable if submitted in a separate, timely filed amendment canceling the non-allowable claim(s).

7. For purposes of appeal, the proposed amendment(s): a) will not be entered, or b) will be entered and an explanation of how the new or amended claims would be rejected is provided below or appended.

The status of the claim(s) is (or will be) as follows:

Claim(s) allowed: _____.

Claim(s) objected to: _____.

Claim(s) rejected: _____.

Claim(s) withdrawn from consideration: _____.

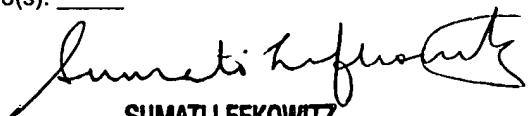
AFFIDAVIT OR OTHER EVIDENCE

8. The affidavit or other evidence filed after a final action, but before or on the date of filing a Notice of Appeal will not be entered because applicant failed to provide a showing of good and sufficient reasons why the affidavit or other evidence is necessary and was not earlier presented. See 37 CFR 1.116(e).
 9. The affidavit or other evidence filed after the date of filing a Notice of Appeal, but prior to the date of filing a brief, will not be entered because the affidavit or other evidence failed to overcome all rejections under appeal and/or appellant fails to provide a showing of good and sufficient reasons why it is necessary and was not earlier presented. See 37 CFR 41.33(d)(1).

10. The affidavit or other evidence is entered. An explanation of the status of the claims after entry is below or attached.

REQUEST FOR RECONSIDERATION/OTHER

11. The request for reconsideration has been considered but does NOT place the application in condition for allowance because:
See Continuation Sheet.
 12. Note the attached Information Disclosure Statement(s). (PTO/SB/08) Paper No(s). _____
 13. Other: _____.



SUMATI LEFKOWITZ
SUPERVISORY PATENT EXAMINER

Continuation of 11. does NOT place the application in condition for allowance because: The proposed will be amended as it does not change the scope of the claims but only overcomes the 35 USC 112, second paragraph rejection for indefiniteness in claim 39, and a claim objection in claim 41. With respect to claims 1-7, applicant argues where the prior art, Chee et al do not render the limitation of highly significant bits to be obvious. Examiner respectfully disagrees. Chee et al disclose in col. 7, lines 58-col. 8, line 2, 25-48 where in different power saving modes, voltage is reduced to non significant items. Further, Chee et al discloses four different power saving modes, where in each mode different power saving functions are reduced. In one of the power saving modes, Chee et al reduces grey scale, thus reducing the insignificant bits of the display, and only displaying the required items. Therefore, the prior art Chee et al renders the highly significant bits obvious. WIth respect to applicant's arguments where Chee et al do not disclose an essential information display mode, Examiner, respectfully, disagrees. Chee et al disclose wherein said power saving mode includes an essential information display mode (col. 5, lines 37-65), where a predetermined uniform voltage level, which corresponds to a predetermined color (col. 5, lines 37-65) and which is independent from said image display data, is uniformly applied to all data electrodes on other region that at least a designated region for displaying the essential information (col. 7, lines 9-44). With respect to applicant's arguments where Chee et al do not disclose wherein a uniform scanning signal is simultaneously applied to all scanning electrodes on other region than said at least designated region for displaying essential information, examiner, respectfully disagrees. Chee et al disclose wherein a uniform scanning signal is simultaneously applied to all scanning electrodes on other region than said at least designated region for displaying the essential information (col. 5, lines 37-48, 66-col. 6, lines 12). WIth resepct to applicant's arguments in regards to where Chee et al do not disclose a partial color display region in power saving mode and where the power saving mode inactivates a gray scale voltage generating circuit, a polarity selecting circuit and an output circuit, examiner, respectfully disagrees. Chee et al disclose wherein at least a partial color display region in said color liquid crystal display is displayed in said power saving mode (col. 7, lines 9-44) and wherein said power saving mode further inactivates a gray scale voltage generating circuit (col. 7, lines 45-57), a polarity selecting circuit, and an output circuit included in a driver circuit for driving said color display (col. 8, lines 25-48). As to dependent claim 39, Chee et al do not disclose generating a plurality of scanning signals by a scanning electrode driver circuit; applying sequentially said plurality of scanning signals to a plurality of scanning electrodes in the color display by controlling said scanning electrode circuit; applying sequentially data signals to said plurality of scanning electrodes by controlling a data electrode driving circuit. Kim discloses in col. 1, lines 14-46, generating a plurality of scanning signals by a scanning electrode driver circuit (col.1, lines 26-27, gate driving circuit), applying sequentially said plurality of scanning signals to a plurality of scanning electrodes in the color display by controlling said scanning electrode circuit (col. 1, lines 26-27, 38-45), applying sequentially data signals to said plurality of data electrodes by controlling a data electrode driving circuit (col. 1, lines 23-25). It would have been obvious to one of ordinary skill in the art at the time the invention was made to include the displaying method of the display device as taught by Kim into the power saving display device of Chee et al as the display method of Kim is a conventional Liquid Crystal Display device (col. 11, lines 14-45 of Kim), and the display of Chee et al is a conventional LCD device. Therefore, the rejection set forth in the Final office action is maintained.